

This Page Is Inserted by IFW Operations  
and is not a part of the Official Record

## **BEST AVAILABLE IMAGES**

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

**IMAGES ARE BEST AVAILABLE COPY.**

**As rescanning documents *will not* correct images,  
please do not report the images to the  
Image Problem Mailbox.**



## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification <sup>7</sup> :

H03F 1/32

A1

(11) International Publication Number:

WO 00/48308

(43) International Publication Date:

17 August 2000 (17.08.00)

(21) International Application Number: PCT/GB00/00194

(22) International Filing Date: 25 January 2000 (25.01.00)

(30) Priority Data:

9903179.1	12 February 1999 (12.02.99)	GB
9907435.3	31 March 1999 (31.03.99)	GB

(71) Applicant (for all designated States except US): WIRELESS SYSTEMS INTERNATIONAL LIMITED [GB/GB]; Innovation House, Bristol Business Park, Coldharbour Lane, Bristol BS16 1EJ (GB).

(72) Inventors; and

(75) Inventors/Applicants (for US only): KENINGTON, Peter [GB/GB]; Trap Farm, Devauden Green, Chepstow NP6 6PE (GB). BATEMAN, Andrew [GB/GB]; Roseacre, 4 Crowe Hill, Limpley Stoke, Bath BA3 6HX (GB).

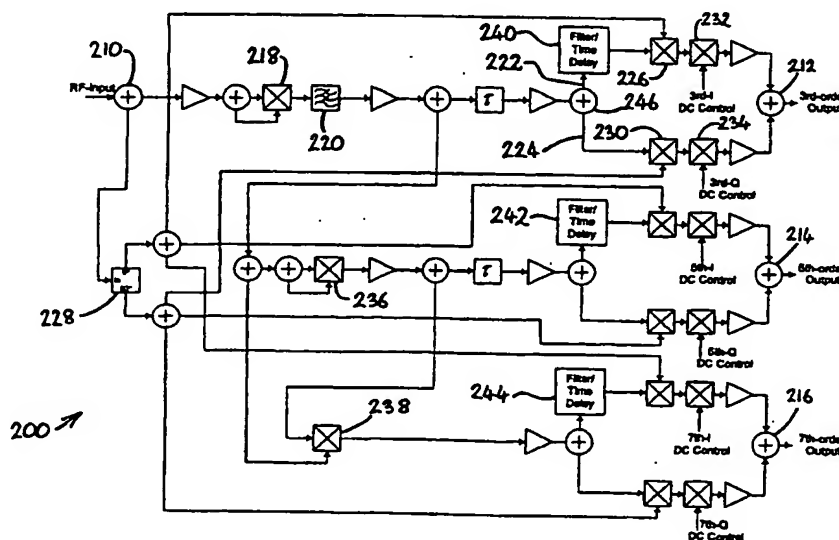
(74) Agents: HOGG, Jeffery, Keith et al.; Withers & Rogers, Goldings House, 2 Hays Lane, London SE1 2HW (GB).

(81) Designated States: AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published

With international search report.

(54) Title: SIGNAL PROCESSING APPARATUS



(57) Abstract

The predistortion circuit (200) predistorts the input signal to a non-linear signal processing device in order to linearise its output. The predistortion circuit (200) derives the predistortion by sampling the input at (210) and multiplies it with itself using mixers (multipliers) (220, 226, 230, 236, etc.) in order to generate various orders of distortion (212, 214, etc.) which are summed to provide the predistortion. Filter/time delay means (240, 242, 244) are incorporated in the paths which generate the orders of distortion in order to introduce a phase and/or amplitude variation with frequency into the distortion. The filter/time delay means can be implemented by adaptive filters in digital signal processing circuits (DSPs). The DSPs can sample the output of the signal processing device being linearised in order to obtain feedback for adapting the filter(s).

**FOR THE PURPOSES OF INFORMATION ONLY**

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece	ML	Mali	TR	Turkey
BG	Bulgaria	HU	Hungary	MN	Mongolia	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MR	Mauritania	UA	Ukraine
BR	Brazil	IL	Israel	MW	Malawi	UG	Uganda
BY	Belarus	IS	Iceland	MX	Mexico	US	United States of America
CA	Canada	IT	Italy	NE	Niger	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NL	Netherlands	VN	Viet Nam
CG	Congo	KE	Kenya	NO	Norway	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NZ	New Zealand	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	PL	Poland		
CM	Cameroon	KR	Republic of Korea	PT	Portugal		
CN	China	KZ	Kazakstan	RO	Romania		
CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
CZ	Czech Republic	LI	Liechtenstein	SD	Sudan		
DE	Germany	LK	Sri Lanka	SE	Sweden		
DK	Denmark	LR	Liberia	SG	Singapore		
EE	Estonia						

## SIGNAL PROCESSING APPARATUS

The present application relates to signal processing apparatus.

In particular, this application relates to signal processing apparatus in which an input signal is subject to intermodulation distortion (IMD) between its input and output. Amplifying circuits, such as those used in transmitters and receivers, can constitute signal processing apparatus of this type.

For example, an RF power amplifier (PA) can, under certain operating conditions, provide an output signal which is a distorted version of its input signal. The distorted output signal can contain IMD products 110, 112 (figure 1) appearing around the output components 114, 116 corresponding to the input spectrum 100.

It is known to compensate for IMD distortion by pre-distorting the input signal spectrum to signal processing apparatus such as a non-linear PA. One form of predistortion generator produces a predistortion comprising controlled quantities of higher order versions of the input signal and introduces it into the input signal prior to the latter's supply to the signal processing means suffering from distortion. In this type of predistorter, the predistortion can contain, for example, third order components created by cubing the input signal and fifth order components created by forming the product of the cube and the square of the input signal.

However, the type of predistorter just discussed does not satisfactorily linearise non-linear signal processing apparatus which inflicts unequal IMD on the input signal. Figure 1 shows an example of an output spectrum of a PA exhibiting unequal IMD of an input spectrum comprising a pair of spaced tones of equal level (represented in the output spectrum as 114 and 116). The IMD components 110 and 112 have different levels and

therefore their cancellation using the predistortion mechanism described above is less effective.

Most (but not all) quasi-linear (class A or AB) RF power amplifiers will exhibit a well-behaved IMD characteristic at narrow tone spacing values (e.g. a few kHz) and this may continue up to a few megahertz or even tens of megahertz in some carefully-designed amplifiers. However, if one considers the previous example of two input tones having the same level, the majority of amplifiers will begin to exhibit a varying degree of inequality between IMD products of the same order as the spacing of the input tones is increased.

There is a wide range of reasons why the IMD products are unequal, and these include AM-AM and AM-PM distortions within the amplifying device being applied at different phases and hence resulting in a degree of subtraction of one or other product (usually accompanied by a degree of addition to other product); imperfect power supply or bias decoupling resulting in baseband (tone-difference) modulations being applied to the signals - this modulation will almost always be partially out-of phase with the IMD products, hence causing the partial cancellation/addition outlined above; and reflection of unterminated harmonic distortion components from e.g. output combining networks causing additional (and again out-of-phase) IMD products to be generated in the amplifier output.

According to one aspect, the invention consists in distorting apparatus for altering an input signal to signal processing means so as to counter unequal intermodulation distortion (IMD) in the output signal of the signal processing means to make it more linear.

Advantageously, the distorting apparatus comprises generating means for generating a distortion signal from the input signal and comprises two parallel signal paths, which each receive the distortion signal, and compensating means located in at least one of the paths, the compensating means introducing a phase and/or amplitude variation with frequency into the distortion signal, and combining means for recombining the signals issuing from the two signal paths.

Preferably the compensating means is filter means or delay means.

In a preferred embodiment, the filter means is a digital filter implemented by a digital signal processor (DSP), and the filter's characteristics are adapted in response to a feedback signal derived from the output signal of the signal processing means which is indicative of IMD components present therein.

Certain embodiments of the present invention will now be described, by way of example only, with reference to the accompanying figures, in which:-

Figure 1 is a diagram of a two-tone test exhibiting unequal IMD products;

Figure 2 is a diagram illustrating a portion of a predistortion generating circuit;

Figure 3 is a diagram illustrating different IMD behaviours with varying tone spacing;

Figure 4 is a diagram of a part of a predistortion circuit incorporating a DSP for generating an arbitrary phase-amplitude characteristic with tone spacing;

Figure 5 is a block diagram of a nonrecursive adaptive filter such as maybe implemented by the DSP shown in figure 4;

Figure 6 is a block diagram of a recursive adaptive filter which maybe implemented by the DSP of figure 4;

Figure 7 is a block diagram of a Hilbert Transform based adaptive filter which maybe implemented by the DSP of figure 4;

Figure 8 illustrates the incorporation of feedback based IMD characteristic optimisation;

Figure 9 illustrates the modification of figures 8 to utilise and injected pilot tone for DSP filter control;

Figure 10 illustrates the use of the DSP to perform additional functions within the predistortion circuit;

Figure 11 illustrates a modification of the predistortion circuit of figure 10, in which the DSP also performs a quadrature splitting function;

Figure 12 is a diagram showing a portion of a filter control mechanism which may be used with the predistortion circuit of figure 10 or 11;

Figure 13 is a diagram illustrating a filter spectrum after adaptation using a single broad band filter;

Figure 14 is a diagram illustrating an example filter spectrum after adaption using a filter bank; and

Figure 15 is a diagram illustrating predistortion of an RF power amplifier to linearise its output.

Figure 15 illustrates a circuit 1500 for linearising the response of a non-linear power amplifier 1510. A portion of the RF input to the amplifying 1510 is removed at coupler 1512 and is diverted from the main signal path to a predistortion circuit 1514. The output of the predistortion circuit 1514 is a distorted version of the input signal and this is amplified by amplifier 1516 prior to being recombined with the main signal path at 1518. The main signal path contains a time delay element 1520 which ensures that the signals travelling through the main and predistortion paths coincide at combiner 1518. The output from combiner 1518 is a predistorted version of the input signal which counters the distortion by the non-linear power amplifier 1510 in order to linearise its output response. The predistortion circuit 1514 can be controlled using a feedback signal derived from the output of the non-linear power amplifier 1510 in order to provide adaptive control, as will be discussed later.

Figure 2 shows a portion 200 of a predistortion circuit 1514 for countering unequal IMD in the output of signal processing apparatus, in this instance a non-linear RF power amplifier (PA) (1510, Figure 15). The input to the PA is tapped (1512, Figure 15) and is provided to the predistortion circuit at 210. In general terms, the predistortion circuit multiplies (or mixes) the input signal with itself to produce distortion components of various orders at 212, 214 and 216, which are recombined to form a predistortion signal which is injected into the input signal (1518, Figure 15) prior to the PA in order to linearise its output.

The predistortion circuit shown generates three separate orders of distortion components: third, fifth and seventh order, each of which is susceptible of individual control. As will be clear to the skilled person, the predistortion circuit can be extended in a simple manner to produce further, higher order distortion components.

The process by which the separate distortion orders are generated is described in detail in earlier patent applications GB 9804835.8 and GB 9804745.9. Briefly, the third order distortion component is generated by squaring the input signal at mixer 218, low pass filtering the resulting signal using filter 220 and supplying the resultant signal to two parallel signal paths 222 and 224. In one of these branches, the signal is mixed (at 226) with the input signal to the PA (from 210), as supplied by the in-phase output (I) of quadrature splitter 228. In the other branch, the signal is mixed (at 230) with the quadrature output (Q) of the quadrature splitter 228. The outputs of mixers 226 and 230 are then each mixed with in-phase and quadrature control (dc) signals at mixers 232 and 234 respectively, in order to generate, on recombination at 212, a third order distortion component having the required phase and amplitude characteristics to optimise the predistortion signal. The I and Q dc control signals applied to mixers 232 and 234 are supplied by, for example, control circuitry (not shown) which monitors residual IMD in the output of the PA. Alternatively, the control circuitry could monitor cross modulation components around a pilot signal, as will be described later. The control circuitry could be implemented by a DSP.

The predistortion circuit is also arranged to generate a fifth order distortion component (at 214). Basically, this is achieved by squaring, using mixer 236, the low-pass-filtered output



of squaring mixer 218, thus generating a fourth-order signal. This fourth order signal is then divided between two signal paths (analogous to paths 222 and 224 used for generating the third order distortion component) where the fourth order signal is mixed with I and Q versions of the sampled PA input signal from quadrature splitter 228, and with I and Q dc control signals from the control circuitry.

Similarly, a seventh order distortion component is produced at mixer 238 by forming the product of the squared PA input from mixer 218 with the fourth order output of mixer 236. The sixth order signal so produced is further mixed with I and Q versions of the PA input signal, etc. in a manner analogous to the generation of the third and fifth order distortion components to produce the independently controlled seventh order distortion component.

In this predistortion circuit, a defined characteristic of phase-shift vs frequency and/or amplitude variation vs frequency characteristic may be introduced in one or both of the parallel signal paths used to generate a given distortion component. This allows a variable (but defined) IMD inequality vs tone spacing to result from a given order of the predistortion circuit. This can then be arranged to match the PA distortion characteristic in this respect and hence provide broad band linearisation.

The frequency-variable characteristic referred to above is provided by a passive network 240 consisting of filter and/or time-delay elements which mimic the behaviour of the amplifier with varying tone-spacing. To take a simple example, the behaviour of the power supply decoupling circuitry can be mimicked using a similar (or even the same) network, if this is the major cause of IMD inequality at given values of tone spacing. This network is inserted into the baseband path of one of the channels which generates an order of the predistortion, e.g. into the low-pass filtered, second-order term of the third-order non-linearity at 240. This network 240 introduces a frequency-variable phase-amplitude characteristic which results in the required partial addition/cancellation process to remove the IMD inequality. The principle may, of course, be extended to any order and its application to 3rd, 5th and 7th orders is shown at 240, 242 and 244.

It is possible to vary the system shown in Figure 2 in a number of ways. For example, the quadrature splitting process 228 may be removed from the input and placed as a part of each non-linearity order. This would not increase complexity, since it merely involves the replacement of in-phase splitters (e.g. 246) with quadrature splitters. (See figure 11 for the use of quadrature splitters).

The mixers may also be replaced by various forms of analogue multiplier without altering the system functionality and, furthermore, the passive filter/time delay networks need not be the same for each order of IMD generation since they may be tailored to suit the observed characteristics of each IMD order, for a given amplifier.

A possible drawback with the predistortion circuit of Figure 2 arises from the use of passive, analogue networks in its attempt to match, at varying input tone frequency spacings, the behaviour of the device (in this case a PA) being linearised. Passive analogue networks restrict the range and type of characteristics which can be accommodated and also the degree of precision provided by the matching itself. The input power range over which good matching may be obtained is also restricted since it is not easy to adapt passive networks for varying input conditions.

A passive, analogue filter network could, for example, be used to compensate for the IMD (310, 320) vs tone frequency spacing  $T$  shown in Figure 3a, which exhibits smooth, continuous variation. However, such a filter network may not be suitable for compensating IMD (310, 320) which varies with tone frequency spacing  $T$  in an irregular, discontinuous manner, as shown in Figure 3b.

As illustrated in Figure 4, the passive, analogue network can be replaced by a DSP in order to provide compensation for an arbitrary IMD vs tone spacing characteristic. This diagram illustrates only the portion of the predistortion circuit which produces the third order distortion component (as will be apparent on comparison with Figure 2), but the principle may be extended to the portions of the previous distortion circuit which produce the distortion components of other orders. The DSP 410 and its associated converters 412 and 414 are arranged to sample at more than twice the maximum bandwidth (tone spacing)

required of the system in order to avoid aliasing effects. When this criteria is fulfilled, the DSP 410 may operate in a number of ways in order to provide the required IMD vs tone spacing characteristic. For example the DSP may perform a Hilbert transformation of its input signal followed by an amplitude based spectrum equalisation or, instead, the DSP may implement an adaptive recursive or non-recursive filter or bank of filters. Additionally, the DSP could perform a combination of the above. It will be apparent that the DSP-based filter characteristic need not be the same for each distortion component for which it is employed.

There are numerous architectures for implementing adaptive digital filters which can be used to perform the gain and phase matching task in the predistortion circuit. Figure 5 shows a block diagram for a standard non-recursive, FIR-based adaptive filter, which is commonly used in communication systems. The coefficients used in the filter are adapted on the basis of detected IMD in the output of the PA being linearised.

Figure 6 shows an equivalent recursive version of the filter, commonly employed with the Kalman adaptive filter algorithm. The coefficients  $a_n, b_n$  are derived from residual IMD in feedback from the PA.

Figure 7 shows an implementation of an adaptive filter using two constant linear phase filters with gain-only adaption. These filters are placed within a Hilbert Transform sub-system and can be independently controlled using in-phase (I) and quadrature (Q) channel correction signals derived by the control system for the predistortion circuit.

Figure 8 extends the embodiment of figure 7 by the incorporation of a quadrature feedback path in order to allow the DSP filter to become adaptive, based on the correction performance achieved by the overall predistortion circuit. This adaption allows the system to optimise its performance at any input power level, tone spacing or operating frequency and hence is an extremely versatile modification. The output from the PA is attenuated at 810 and is demodulated using a quadrature modulator to provide I and Q correction signals which undergo analogue to digital conversion and are supplied to the DSP in order to provide the I-control and Q-control signals of Figure 7.

It is also possible to use the correlation of the cross-modulation products appearing around a pilot tone as a measure of the residual system distortion of a particular order. A pilot tone based predistortion control mechanism is disclosed in GB 9814391.0. Basically, a pilot tone can be injected into, e.g., the input to a PA before its supply to the PA and cross modulation distortion (CMD) on the pilot signal from the input signal is used as feed back in the control system for the predistortion circuit (since the CMD on the pilot and IMD on the input being amplified are related, minimisation of the CMD equates to minimisation of the IMD). The same DSP could be used to provide the control signals for each order of distortion (i.e. the third-I DC control and third-Q DC control signals shown in Figure 8) and to perform the filtering operations, and would be able to assess the level of the remaining and cancelled IMD and thereby vary the filter parameters in a logical sequence in order to further reduce the IMD. This mechanism has the advantage that only narrow band sampling of the feedback (correlated) signals is required and no additional local oscillator is necessary. It has the potential disadvantage, however, that only a single overall measure of IMD level is provided, which does not yield separate information on the relative IMD levels and differing tone spacing in a multi-tone signal.

In such a system, in which cross modulation distortion on a pilot tone is used to derive the control signals for the predistortion circuit, the pilot tone may be used with the quadrature down conversion process as shown in Figure 9. This allows coherent down conversion of the cross modulation products and hence provides a measure of the IMD level vs tone spacing in a multi-tone environment. The filter adaption may therefore be based directly upon this information.

It is possible to extend the above approach a stage further and incorporate the DSP within the "base band" section of the predistortion circuit as illustrated in Figure 10. This allows the use of independent filters for the inphase (I) and quadrature (Q) channels and a more direct correlation with the quadrature control signals, thus making the control algorithm more simple.

Figure 11 shows a modified version of the figure 10 embodiment, in which the quadrature splitting operation also occurs within the DSP, thus giving the advantage of increased accuracy for the quadrature splitting process and hence provide greater levels of IMD cancellation.

Figure 12 illustrates a possible algorithm for controlling the adaption properties of a non-recursive adaptive filter implemented by a DSP. The technique uses the Least Mean Squares Error (LMSE) approach which is commonly used for filters in the communications field. (Other possible adaption algorithms include Recursive Least Squares and Stochastic Gradient). The filter allows a smooth adjustment of the gain and phase weighting of the predistortion correction signal over the chosen frequency band as illustrated in Figure 13. If more discrete correction of the distortion components is required with frequency, then a bank of filters may be used, as shown in Figure 14.

Figure 14 shows a conceptual example of the adapted filter frequency response for one of the I or Q channels. It can be seen that differing gain values have been applied to differing frequencies within the base band signals spectrum, using a "comb" of bandpass filters. Each individual filter may be adapted in order to optimise IMD cancellation, with the filters in the I and Q channels operating completely independently of one another.

## Claims

1. Distorting apparatus for altering an input signal to signal processing means so as to counter unequal intermodulation distortion (IMD) in the output signal of the signal processing means to make it more linear.
2. Apparatus according to claim 1, further comprising distortion generating means for generating a distortion signal from the input signal for introduction into the input signal and compensating means for introducing a phase and/or amplitude variation with frequency into the distortion signal.
3. Apparatus according to claim 2, further comprising distortion generating means for generating at least one further distortion signal from the input signal for introduction into the input signal and compensating means for introducing a phase and/or amplitude variation with frequency into the or each further distortion signal
4. Apparatus according to any preceding claim, comprising generating means for generating a distortion signal from the input signal, two parallel signal paths, which each receive the distortion signal, compensating means located in at least one of the paths, the compensating means introducing a phase and/or amplitude variation with frequency into the distortion signal, and combining means for recombining the signals issuing from the two signal paths.
5. Apparatus according to claim 4, comprising compensating means in each signal path.
6. Apparatus according to claim 4 or 5, wherein the or each compensating means comprises filter means or delay means.
7. Apparatus according to claim 6, wherein the compensating means comprises filter means.

8. Apparatus according to claim 7, wherein the filter means is arranged to perform any of, or any combination of, recursive filtering, non-recursive filtering or, using a bank of filters, discrete filtering.
9. Apparatus according to claim 7 or 8, wherein the characteristics of the filter means are adapted in response to a feedback signal derived from the output signal of the signal processing means which is indicative of distortion present therein.
10. Apparatus according to claim 9, comprising means for injecting a pilot signal into the input signal, wherein the characteristics of the filter means are adapted in response to distortion of the pilot signal present in the feedback signal derived from the output signal of the signal processing means.
11. Apparatus according to any one of claims 7 to 10, comprising control means for adapting the characteristics of the filter means on the basis of a least mean squares error or recursive least mean squares error algorithm.
12. Apparatus according to any one of claims 2 to 11, comprising Hilbert Transform means operating on a distortion signal to produce a filter input signal for each of two filters in parallel and control means which quadrature demodulates a feedback signal from the output signal to produce a secondary feedback signal for each of the parallel filters.
13. Distorting apparatus substantially as described with reference to at least one of the figures.
14. A method of linearising an output signal of signal processing means comprising the step of altering an input signal to the signal processing means so as to counter an unequal intermodulation distortion (IMD) in the output signal of the signal processing means.

15. A method according to claim 14, further comprising the steps of generating a distortion signal from the input signal for introduction into the input signal and introducing a phase and/or amplitude variation with frequency into the distortion signal.
16. A method according to claim 15, comprising the steps of generating at least one further distortion signal from the input signal and introducing a phase and/or amplitude variation with frequency into the or each further distortion signal.
17. A method according to claim 15 or 16, wherein the or each step of generating a distortion signal from the input signal, is succeeded by the steps of supplying it to two parallel signal paths, introducing a phase and/or amplitude variation with frequency into the distortion signal in at least one of paths, and recombining the signals issuing from the two signal paths.
18. A method according to claim 17, comprising the step of introducing a phase and/or amplitude variation with frequency in each parallel signal path.
19. A method according to any one of claims 15 to 18, wherein the introducing step or steps comprises filtering or delaying means.
20. A method according to claim 19, wherein the introducing step or steps comprises a filtering step.
21. A method according to claim 20, wherein the filtering step comprises any of, or any combination of, recursive filtering, non-recursive filtering or, using a bank of filters, discrete filtering.
22. A method according to claim 20 or 21, wherein the characteristics of the filtering are adapted in response to a feedback signal derived from the output signal of the signal processing means which is indicative of distortion present therein.



23. A method according to claim 22, comprising the step of injecting a pilot signal into the input signal, adapting the characteristics of the filtering in response to distortion of the pilot signal present in the feedback signal derived from the output signal of the signal processing means.

24. A method according to any one of claims 20 to 23, comprising the step of controlling the adaption of the characteristics of the filtering on the basis of a least mean squares error or recursive least mean squares error algorithm.

25. A method according to any one of claims 15 to 24, comprising the steps of performing a Hilbert Transform on a distortion signal to produce a filter input signal for each of two filters in parallel and quadrature-demodulating a feedback signal from the output signal to produce a secondary feedback signal for each of the parallel filters.

26. A method of linearising an output signal substantially as described with reference to at least one of the figures.

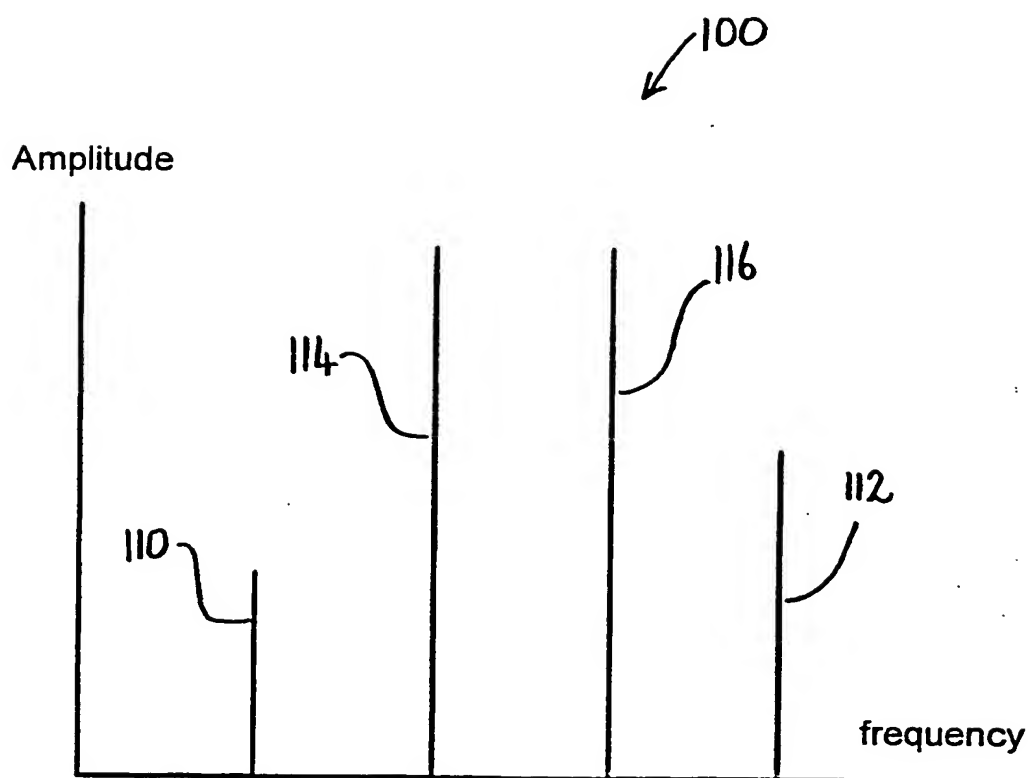


Figure 1

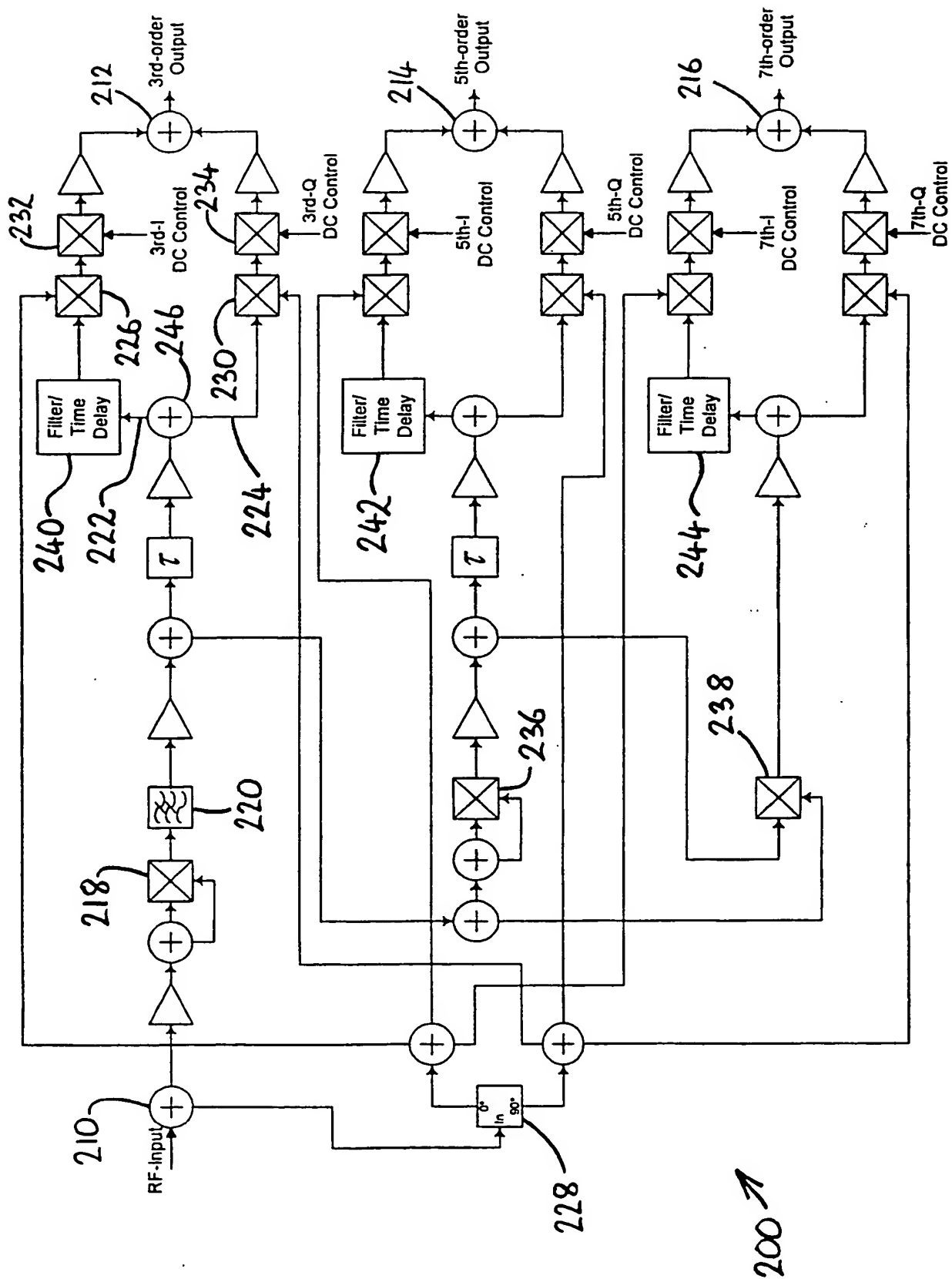


Figure 2

3/15

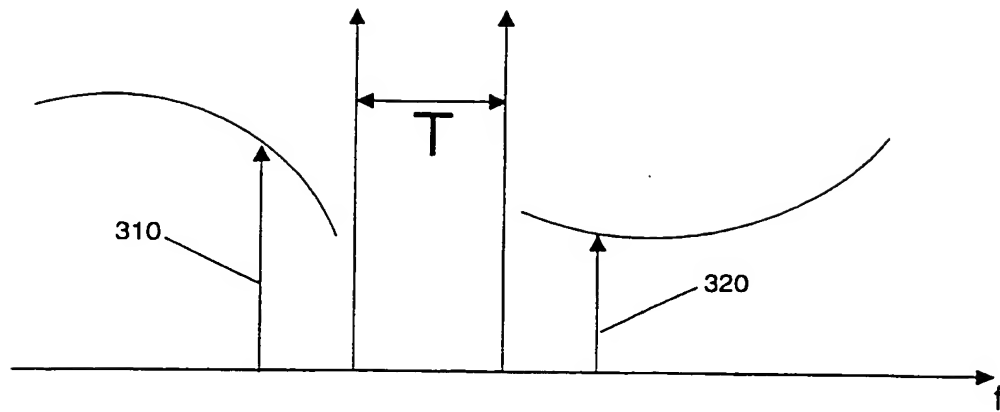


Figure 3a

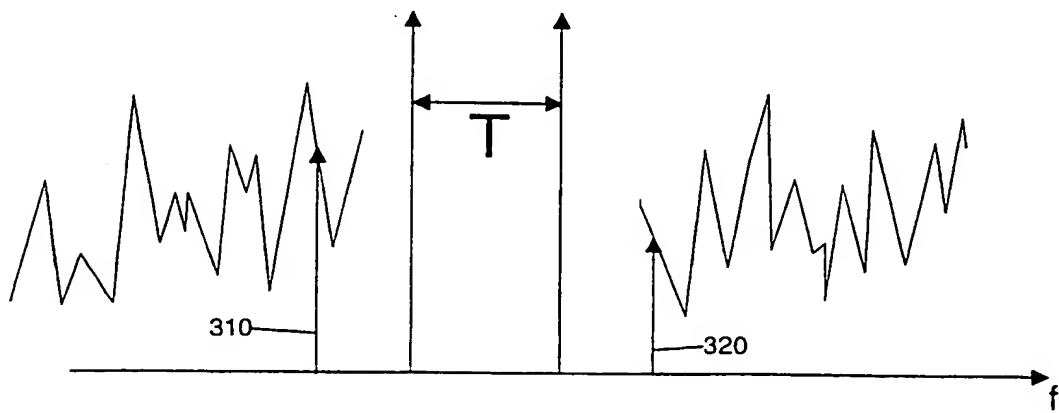


Figure 3b



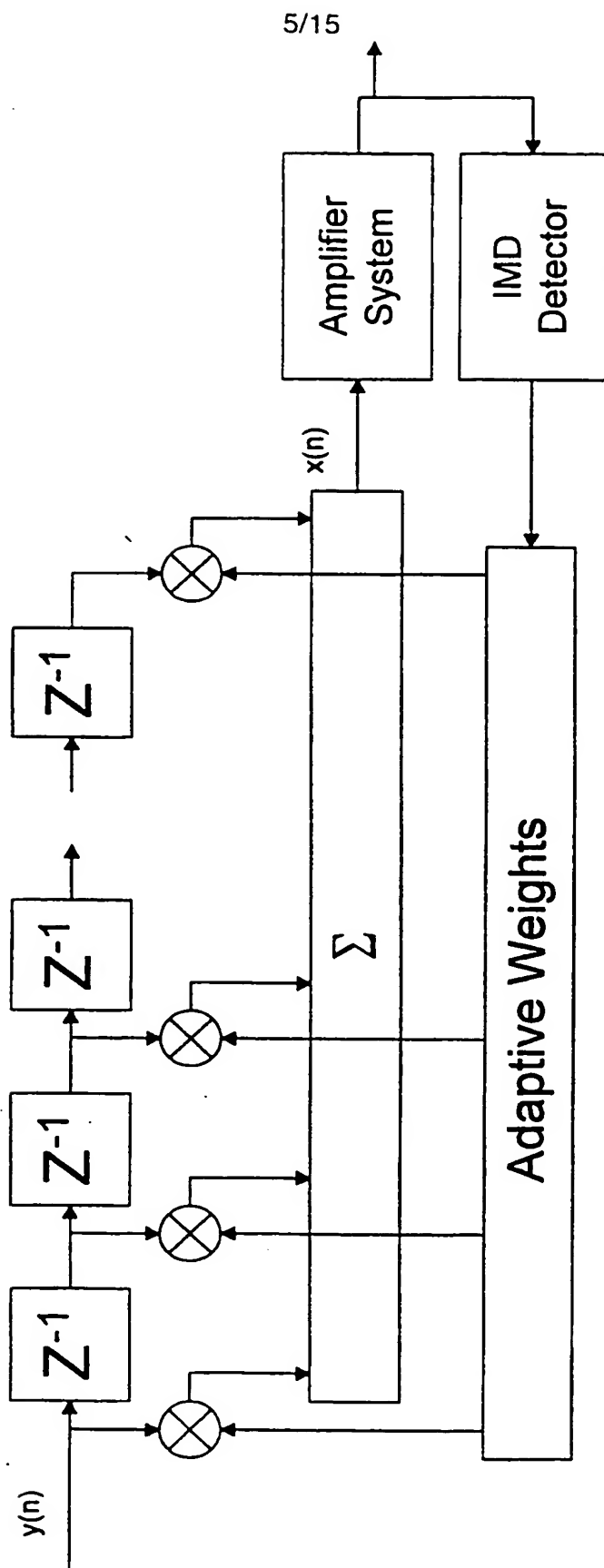


Figure 5

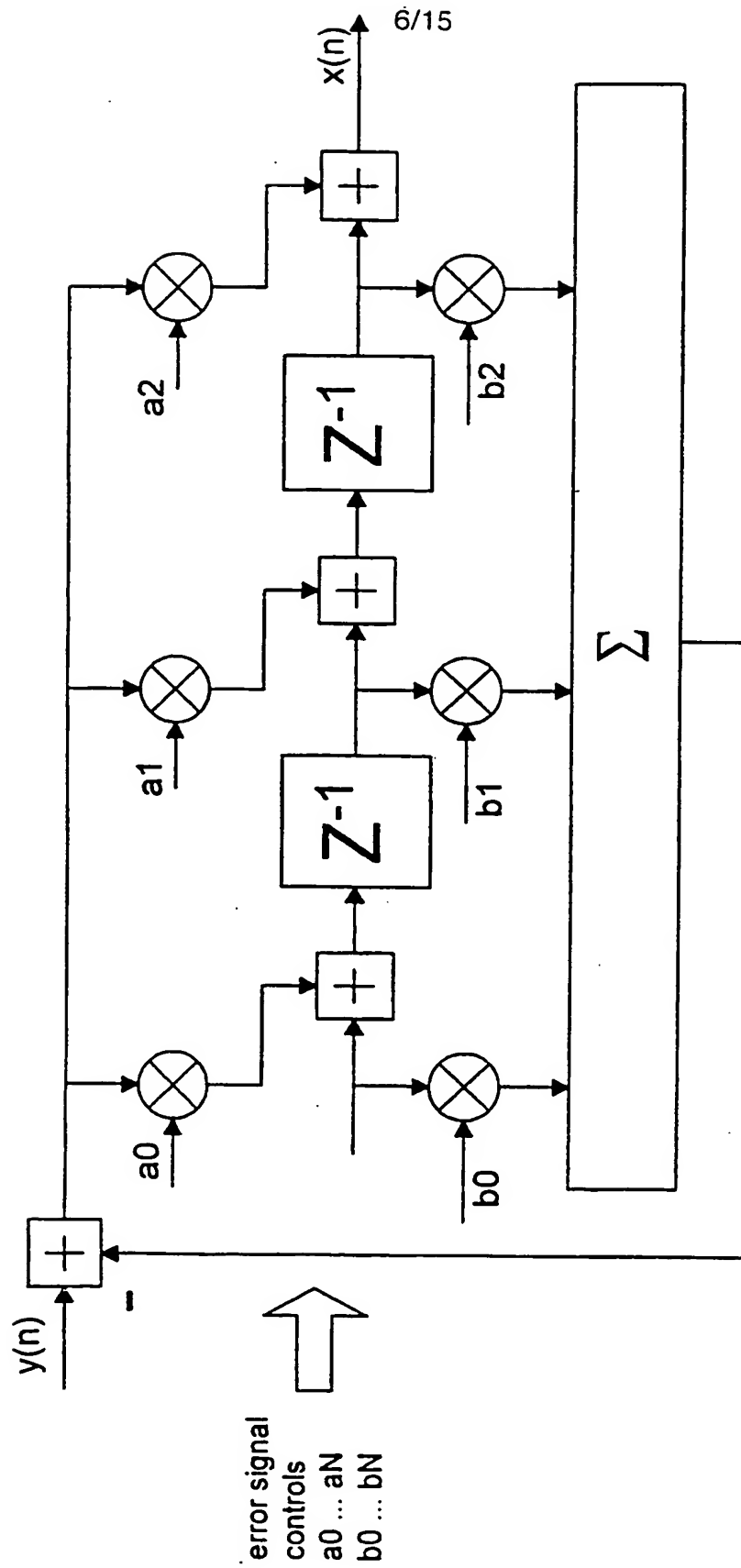


Figure 6

7/15

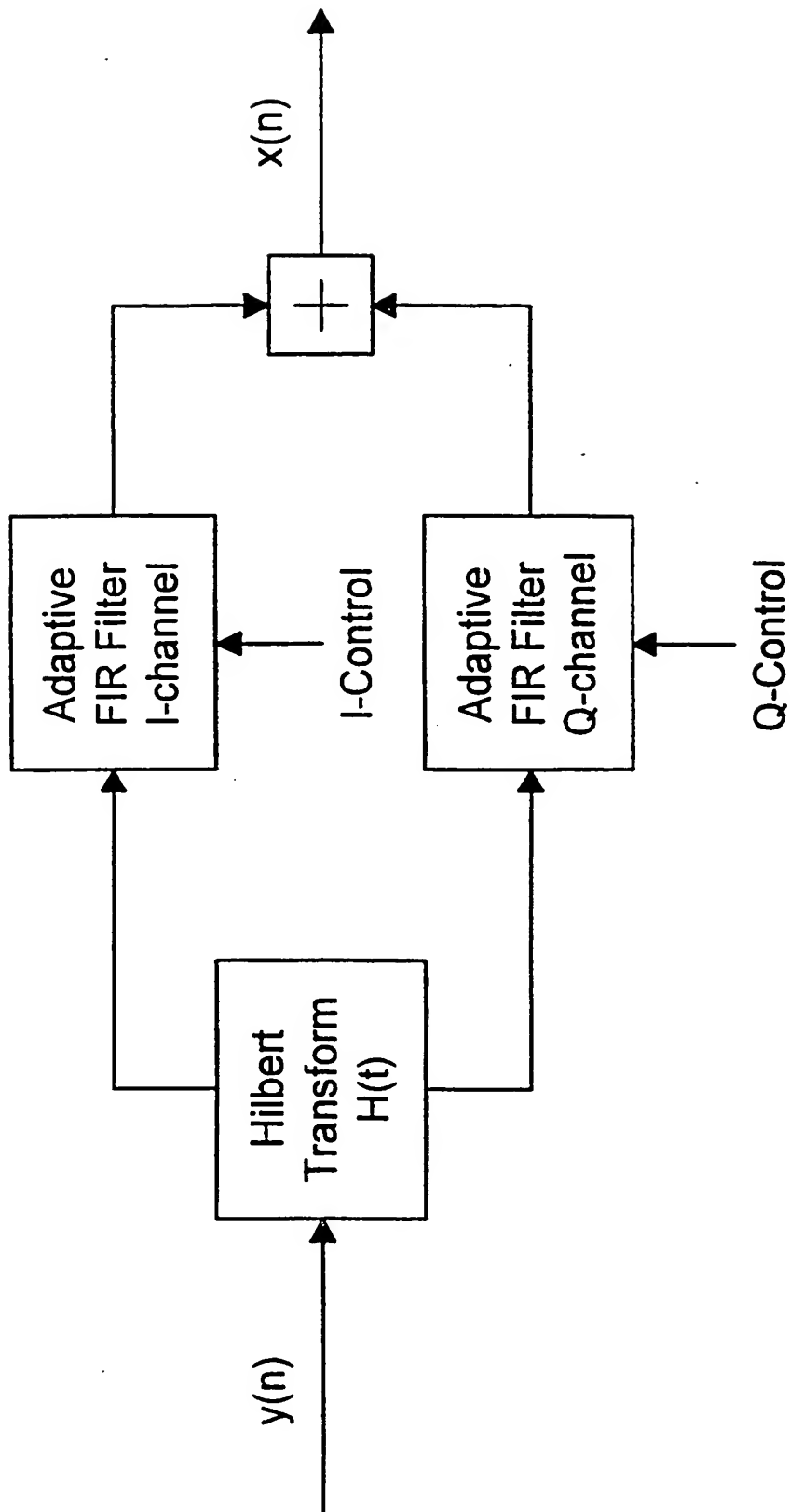


Figure 7



8/15

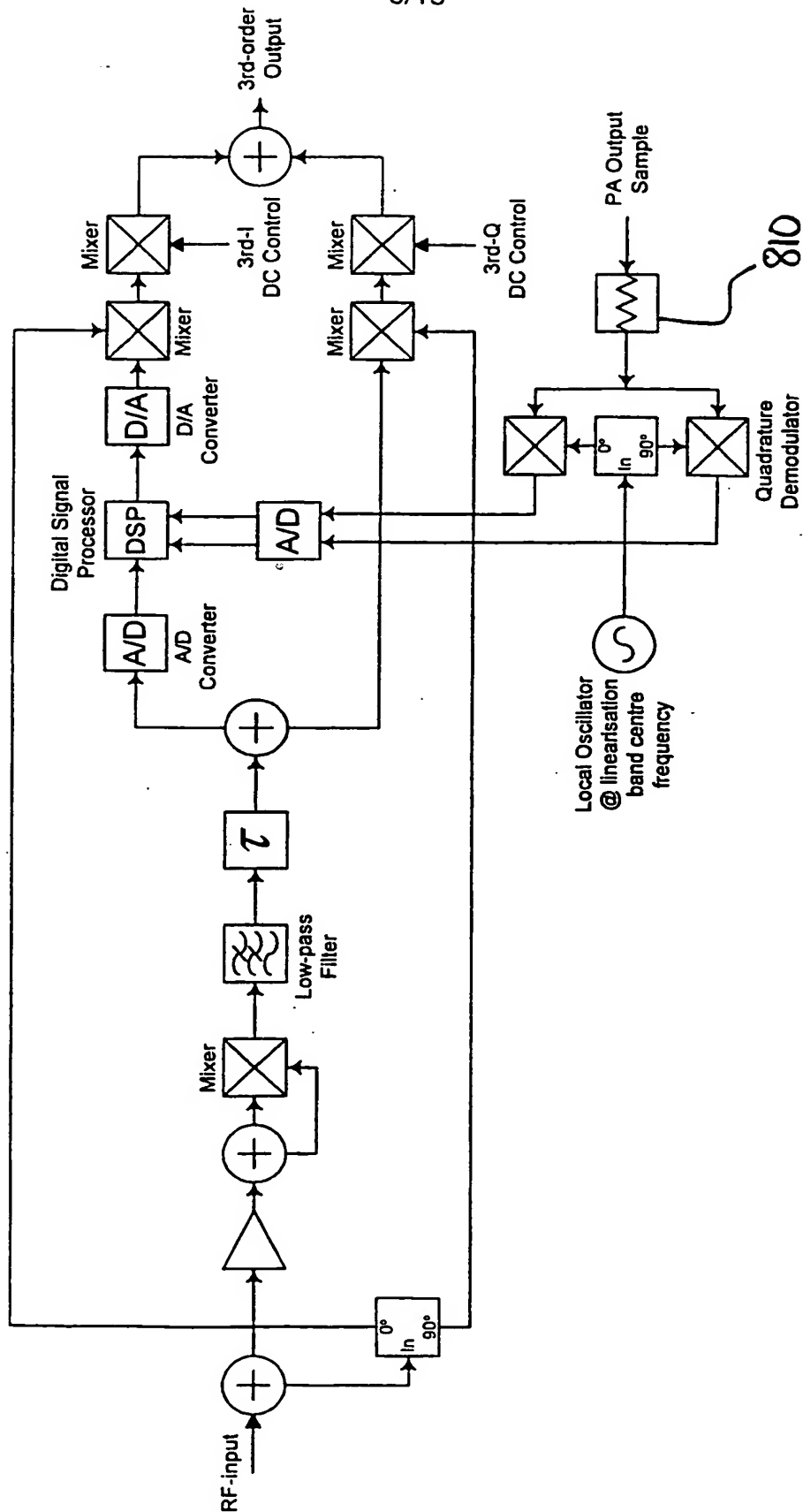


Figure 8

9/15

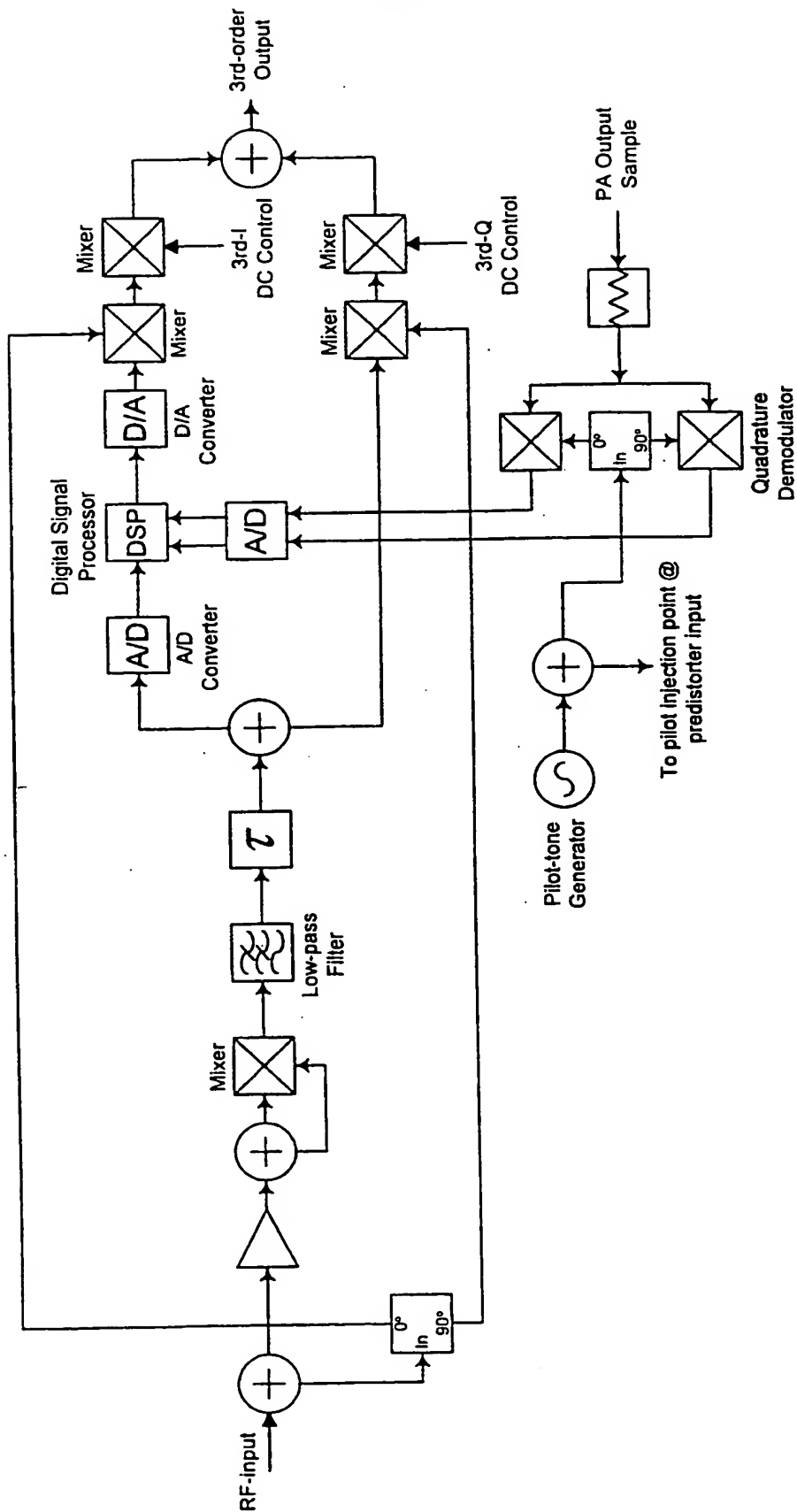


Figure 9

10/15

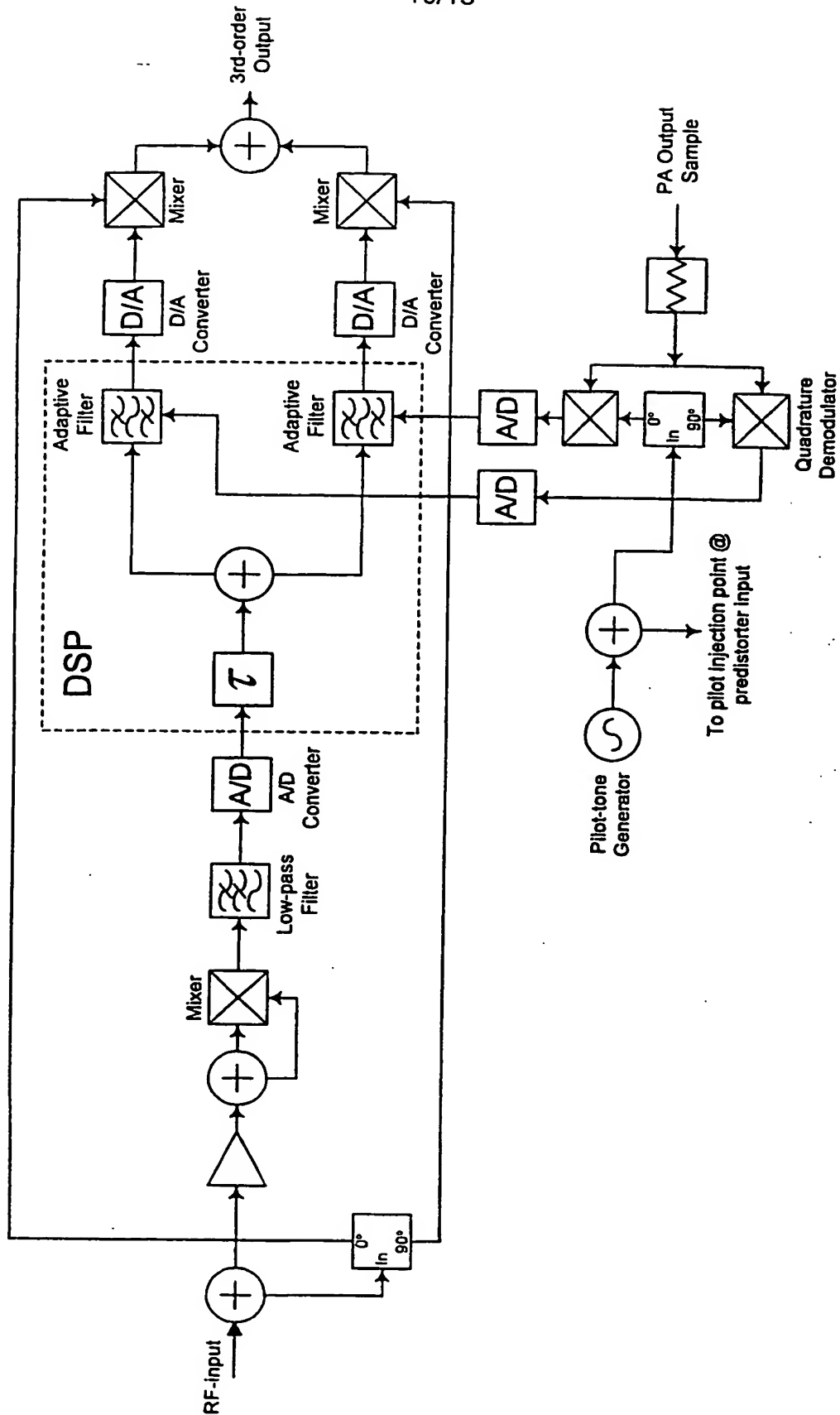


Figure 10

11/15

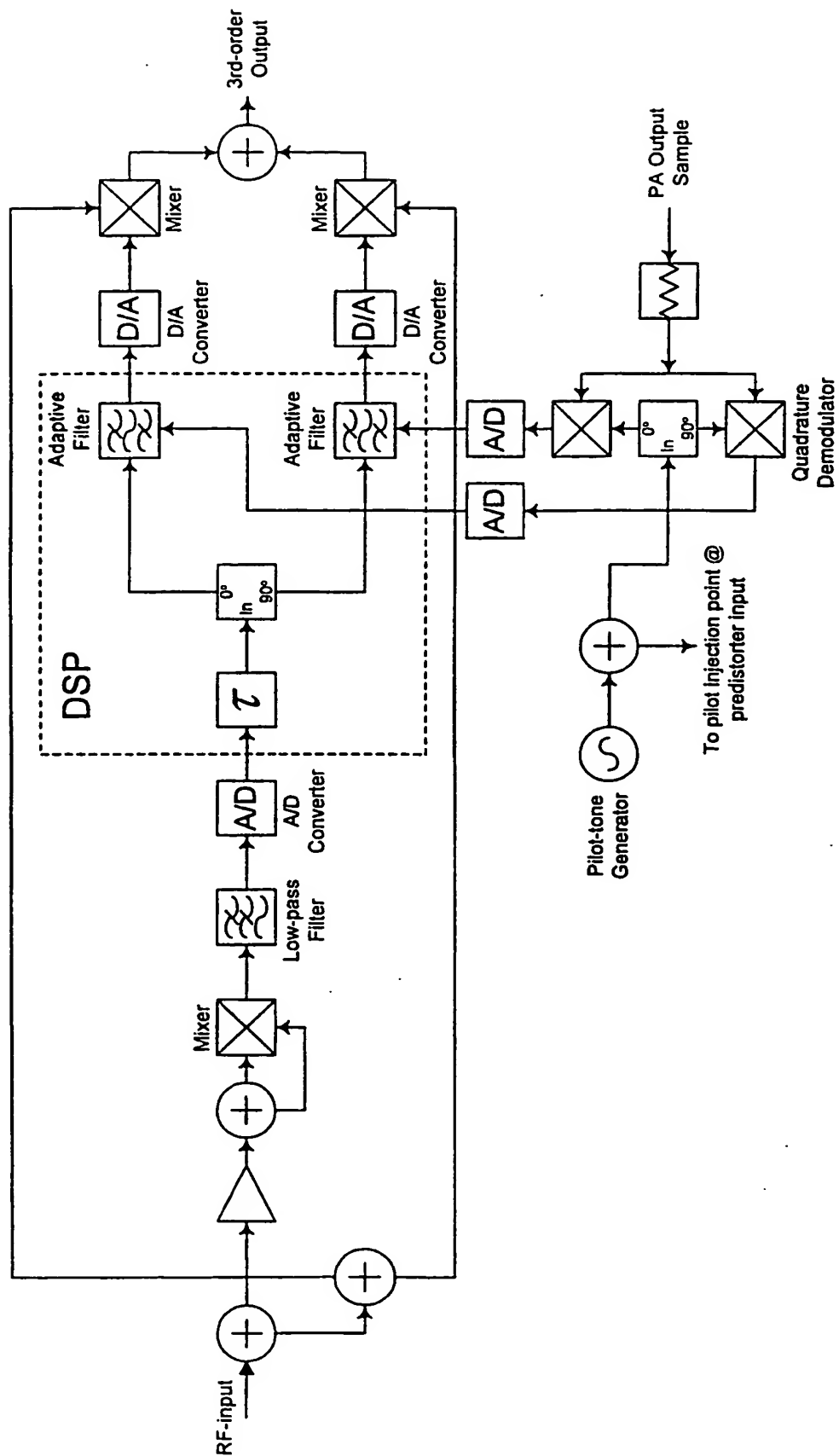


Figure 11

12/15

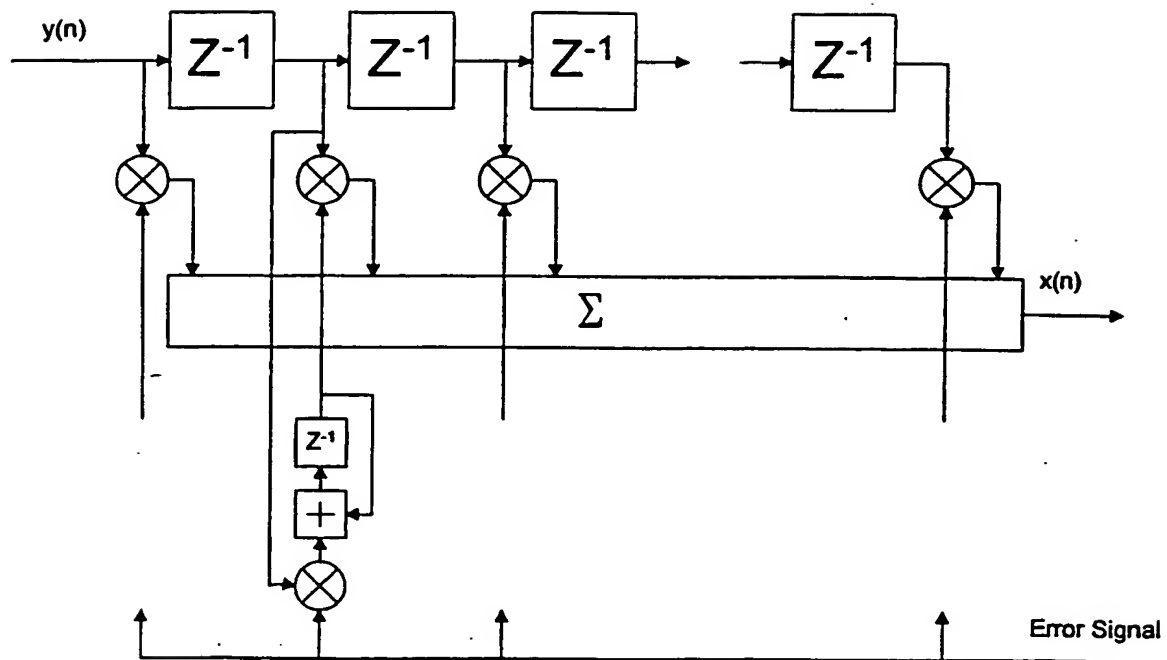


Figure 12

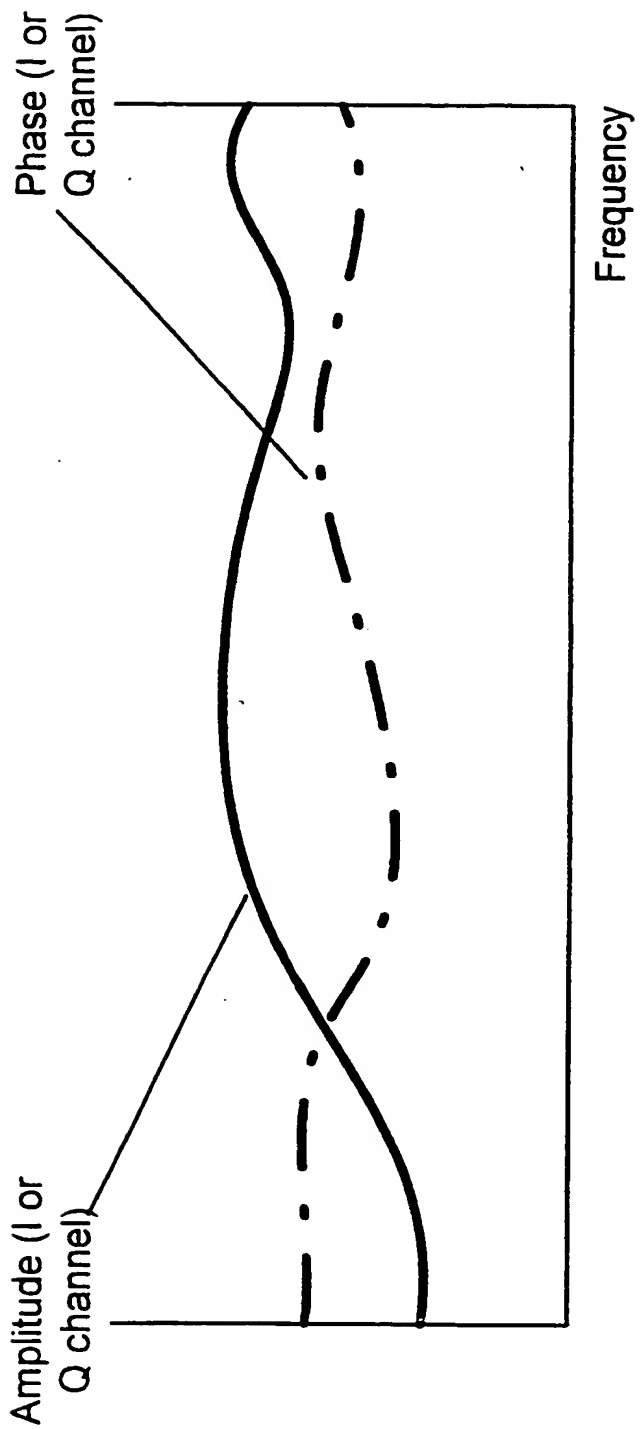


Figure 13

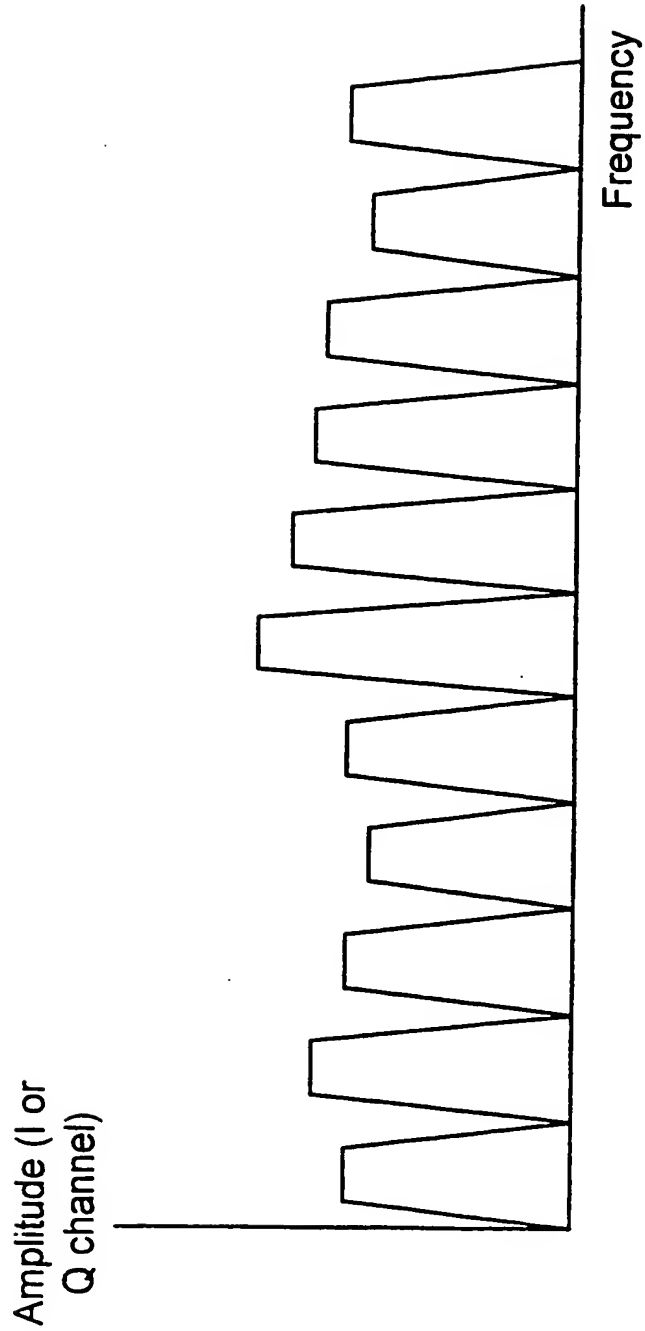
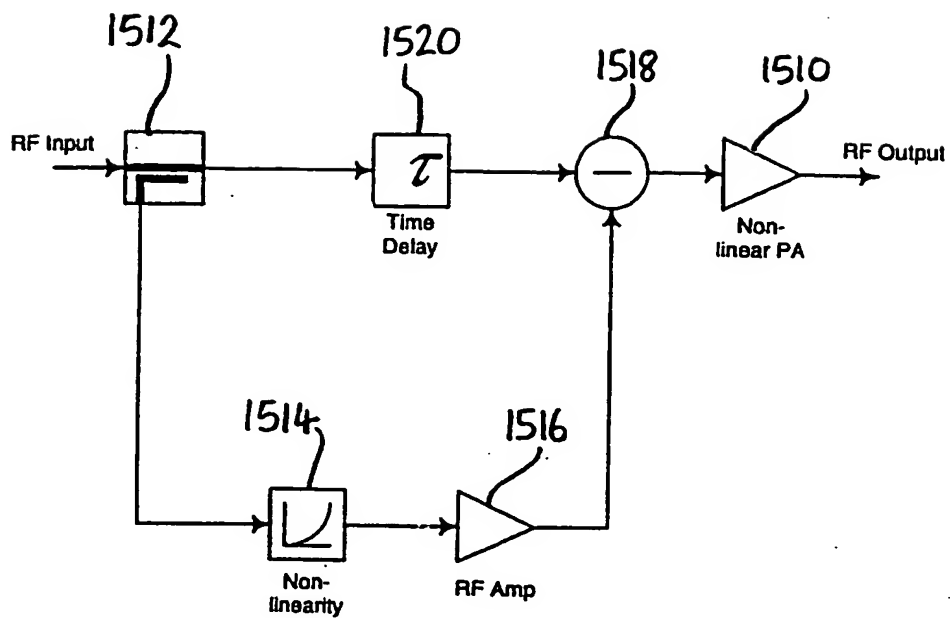


Figure 14



1500

Figure 15



# INTERNATIONAL SEARCH REPORT

International Application No

PCT/GB 00/00194

## A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H03F1/32

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 093 637 A (SUEMATSU NORIHARU ET AL) 3 March 1992 (1992-03-03) abstract; figure 2	1, 14
X	US 5 770 971 A (MCNICOL JOHN DUNCAN) 23 June 1998 (1998-06-23) abstract; figure 2	1, 14
P, X, L	WO 99 45640 A (KENNINGTON PETER ; WIRELESS SYSTEMS INTERNATIONAL (GB)) 10 September 1999 (1999-09-10) cited in the application the whole document	1-26
P, X, L	WO 00 02324 A (KENNINGTON PETER ; WIRELESS SYSTEMS INTERNATIONAL (GB)) 13 January 2000 (2000-01-13) the whole document	1-26

-/-

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

### \* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search

25 April 2000

Date of mailing of the international search report

02/05/2000

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  
Fax: (+31-70) 340-3016

Authorized officer

Segaert, P

# INTERNATIONAL SEARCH REPORT

Intern: al Application No

PCT/GB 00/00194

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
P,X, L	WO 99 45638 A (KENNINGTON PETER ; WIRELESS SYSTEMS INTERNATIONAL (GB)) 10 September 1999 (1999-09-10) cited in the application the whole document	1-26
X	US 5 164 678 A (BOKSBERGER HANS ULRICH ET AL) 17 November 1992 (1992-11-17) the whole document	1-24
X	US 5 424 680 A (GALL CHARLES H ET AL) 13 June 1995 (1995-06-13) the whole document	1-24
X	EP 0 054 811 A (LICENTIA GMBH) 30 June 1982 (1982-06-30) the whole document	1-24

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/GB 00/00194

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
US 5093637	A	03-03-1992	JP 2038800 C	28-03-1996
			JP 3198555 A	29-08-1991
			JP 7071118 B	31-07-1995
			CA 2033301 A	28-06-1991
			CA 2033301 C	07-06-1994
			GB 2239770 A, B	10-07-1991
US 5770971	A	23-06-1998	BR 9706673 A	30-03-1999
			CA 2211231 A	26-01-1998
WO 9945640	A	10-09-1999	AU 2737399 A	20-09-1999
			GB 2335812 A, B	29-09-1999
WO 0002324	A	13-01-2000	GB 2339354 A	19-01-2000
WO 9945638	A	10-09-1999	AU 2737099 A	20-09-1999
			GB 2335813 A, B	29-09-1999
US 5164678	A	17-11-1992	EP 0465709 A	15-01-1992
			BR 9102948 A	11-02-1992
			CA 2046457 A	13-01-1992
			CN 1058122 A, B	22-01-1992
			CS 9102104 A	19-02-1992
			HU 60576 A	28-09-1992
			JP 4233810 A	21-08-1992
			PL 290991 A	10-08-1992
			ZA 9105313 A	25-03-1992
US 5424680	A	13-06-1995	EP 0746902 A	11-12-1996
			WO 9515613 A	08-06-1995
EP 0054811	A	30-06-1982	DE 3047292 A	29-07-1982
			DK 556281 A	17-06-1982
			JP 57150215 A	17-09-1982